



Core Information Model (CoreModel)

TR-512.A.6 Appendix – Packet Switched Examples (Layer 2 & Layer 3)

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Important note

This Technical Recommendations has been approved by the Project TST, but has not been approved by the ONF board. This Technical Recommendation is an update to a previously released TR specification, but it has been approved under the ONF publishing guidelines for 'Informational' publications that allow Project technical steering teams (TSTs) to authorize publication of Informational documents. The designation of '-info' at the end of the document ID also reflects that the project team (not the ONF board) approved this TR.

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Document History

Version	Date	Description of Change
		Appendix material was not published prior to Version 1.3
1.4	November 2018	Initial Version 1.4
1.5	September 2021	Enhancements to model structure
1.6	January 2024	Updated release and dates.

1 Introduction to the document suite

This document is an appendix of the addendum to the TR-512 ONF Core Information Model and forms part of the description of the ONF-CIM. For general overview material and references to the other parts refer to [TR-512.1](#).

1.1 References

For a full list of references see [TR-512.1](#).

1.2 Definitions

For a full list of definition see [TR-512.1](#).

1.3 Conventions

See [TR-512.1](#) for an explanation of:

- UML conventions
- Lifecycle Stereotypes
- Diagram symbol set

1.4 Viewing UML diagrams

Some of the UML diagrams are very dense. To view them either zoom (sometimes to 400%) or open the associated image file (and zoom appropriately) or open the corresponding UML diagram via Papyrus (for each figure with a UML diagram the UML model diagram name is provided under the figure or within the figure).

1.5 Understanding the figures

Figures showing fragments of the model using standard UML symbols and also figures illustrating application of the model are provided throughout this document. Many of the application-oriented figures also provide UML class diagrams for the corresponding model fragments (see [TR-512.1](#) for diagram symbol sets). All UML diagrams depict a subset of the relationships between the classes, such as inheritance (i.e. specialization), association relationships (such as aggregation and composition), and conditional features or capabilities. Some UML diagrams also show further details of the individual classes, such as their attributes and the data types used by the attributes.

1.6 Appendix Overview

This document is part of the Appendix to TR-512. An overview of the Appendix is provided in [TR-512.A.1](#).

2 Introduction to this Appendix document

This document provides various examples of the use of the CIM to model packet switched network structures.

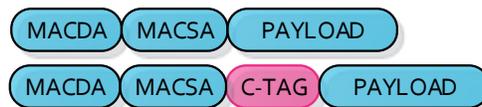
The examples in this document are built from descriptions in earlier referenced works.

3 General examples

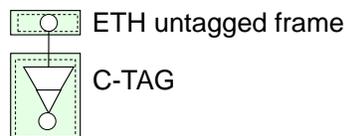
This document introduces a model of layer protocols for packet switched systems, initially in the context of a basic Ethernet device showing the ports and then in the context of a more sophisticated Ethernet device.

This document is not intent to be exhaustive for showing all possible examples. It just shows some port configurations to enable the designer to understand how the mechanism works.

3.1 Basic Ethernet device



(a) Frame example



(b) C-TAG server with an untagged Ethernet client

Figure 3-1 Basic Ethernet device

Figure 3-1 shows how ETH untagged frames could be mapped into C-VLAN with a customer VLAN tag (C-TAG).

3.2 Sophisticated Ethernet device

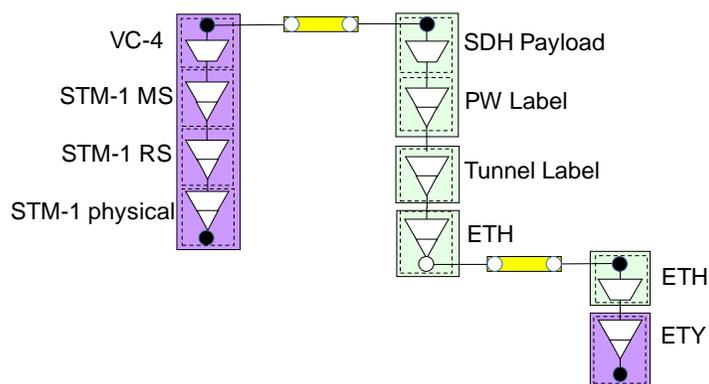


Figure 3-2 STM-1 Circuit Emulation Service over Ethernet

Figure 3-2 shows a device that supports SDH and Ethernet ports with a circuit switching capability at the VC-4 layer and a packet switching capability at the Ethernet layer. The STM-1 tributary signal is terminated up to the VC-4, which is then forwarded by an FC to a multiplexing function that encapsulates the VC-4 first into a pseudowire which is then encapsulated into an MPLS LSP which is then mapped into Ethernet. The Ethernet signal is forwarded via a packet switching FC to the Ethernet port.

If the VC-4 FC is fixed (e.g., in a pure Ethernet device), one can bind the VC-4 directly to the SDH Payload without the need of the FC.

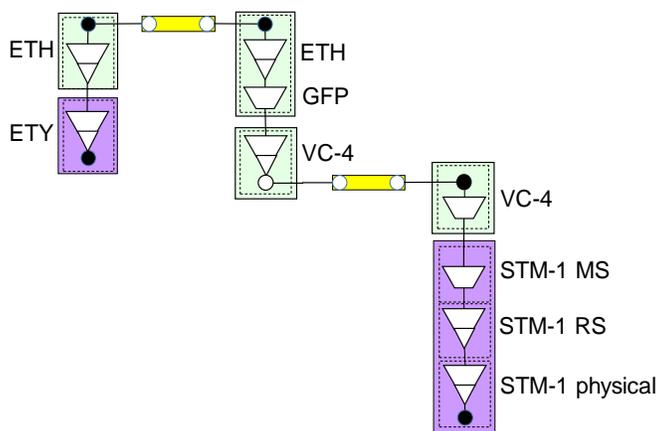


Figure 3-3 Ethernet over SDH (EOS)

Figure 3-3 shows also a device that supports SDH and Ethernet ports with a circuit switching capability at the VC-4 layer and a packet switching capability at the Ethernet layer. The Ethernet signal at the tributary interface is forwarded via a packet switching FC to an Ethernet to SDH mapping function which uses GFP-F. The VC-4 is then forwarded by an FC to the STM-1 port. Note that the example shown in Figure 3-3 is not a typical case in today’s network. The intent of the example is to show the inversion with respect to the example of the STM-1 Circuit Emulation Service over Ethernet shown in Figure 3-2.

4 Ethernet examples

4.1 Ethernet single-layer example

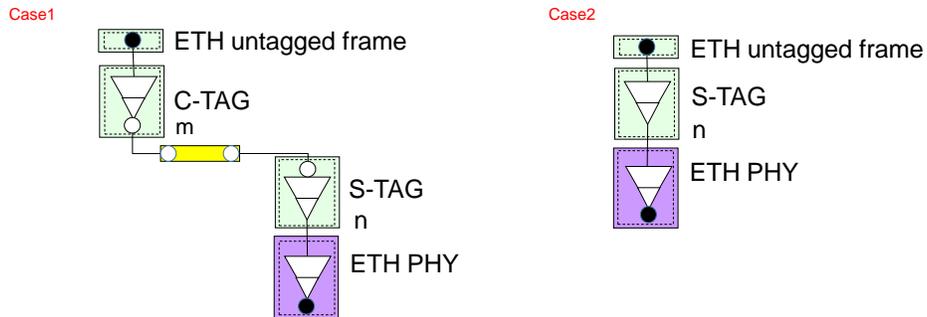


Figure 4-1 802.3 PHY server with S-TAG client

Ethernet untagged, C-tagged and S-tagged frames are all in a single Ethernet layer. Figure 4-1 contains two examples that show how untagged Ethernet frames could be carried by the ETH Physical Layer.

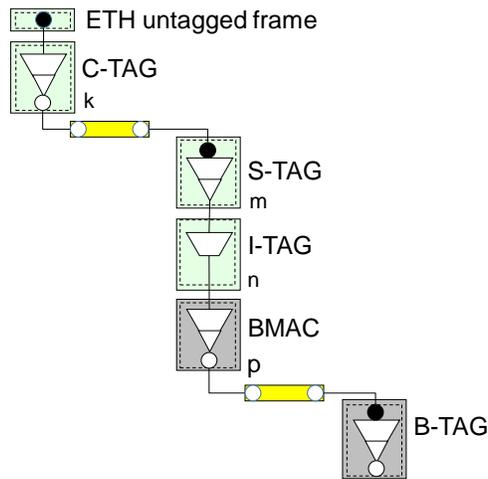
In case 1, a C-TAG is added to the untagged frames; these C-tagged frames are then forwarded via a packet switching FC to the Ethernet port where an S-TAG is added before the signal is going on the wire.

Only an S-TAG is added to the untagged frames in case 2; there is no packet switching involved.

4.2 Ethernet multi-layer example



(a) Frame example



(b) BMAC server with S-TAG client

Figure 4-2 Ethernet multi-layer example

The ETH untagged frames are first C-tagged, then packet switched to a multiplexing function which adds S- and I-TAGs and encapsulates the signal as payload into a new MAC frame. These frames are packet switched to function which adds a B-TAG.

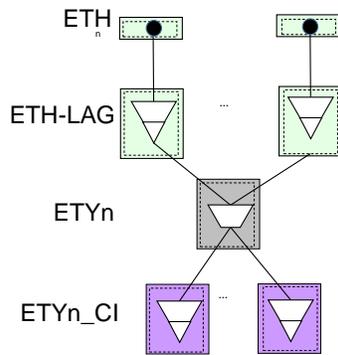


Figure 4-3 LAG example

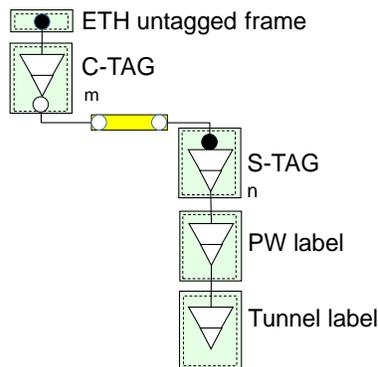
Figure 4-3 introduces the LAG (Link Aggregation Group) model. The Ethernet frames are grouped into an ETH-LAG. One or more of these ETH-LAG clients are combined and then distributed onto more than one ETYn_CI (Characteristic Information) servers.

The LAG example in Figure 4-3 shows only one level of inverse multiplexing, whereas the general pattern of inverse multiplexing described in Section 4.2.3 & Figure 4-16 of [TR-512.2](#) can be applied to multiple levels of inverse multiplexing.

4.3 Ethernet & MPLS-TP multi-layer example



(a) Frame example



(b) MPLS-TP LSP server with MPLS-TP PW client with S-TAG Ethernet client

Figure 4-4 Ethernet & MPLS-TP multi-layer example

Figure 4-4 shows an Ethernet signal that is encapsulated into an MPLS LSP. A C-TAG is added to the untagged frames before they are forwarded via a packet switching FC to the MPLS encapsulation function. Here an S-TAG, a pseudowire label and an MPLS label is then added.

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